



Attorney Docket No. 0756-7196

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Shunpei YAMAZAKI et al.

) Group Art Unit: 2813  
) Examiner: H. Doty

Serial No. 10/659,585

) CERTIFICATE OF MAILING

Filed: September 11, 2003

) I hereby certify that this correspondence is  
) being deposited with the United States Postal  
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) Mail in an envelope addressed to:  
) Commissioner for Patents, P.O. Box 1450,  
) Alexandria, VA 22313-1450, on 5-30-2006

For: MANUFACTURING METHOD OF  
SEMICONDUCTOR DEVICE

CORRECTION TO PREVIOUSLY FILED

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

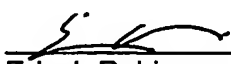
It has come to applicant's attention that an Information Disclosure Statement previously submitted included an inadvertent typographical error or incomplete information.

Specifically, the Information Disclosure Statement filed September 11, 2003 included the following inaccuracy:

1) The Tsutsui et al. article incorrectly reflected "Part 12B" and should have been "Part 2, No. 12B."

The above inaccuracy is corrected on the 1449 Form attached hereto. The Examiner is requested to initial the attached PTO Form 1449 evidencing consideration of the above references and line through these references on the previously submitted Information Disclosure Statements to ensure that the correct information is printed correctly on any issued patent.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285  
Robinson Intellectual Property Law Office, P.C.  
PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165  
(571) 434-6789

OK to enter. HD 6/19/06

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

## Complete if Known

Application Number 10/659,585  
Filing Date September 11, 2003  
First Named Inventor Shunpei YAMAZAKI et al.  
Group Art Unit 2813  
Examiner Name H. Doty  
Attorney Docket Number 0756-7196

## U.S. PATENT DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>3</sup> (if known)			

## FOREIGN PATENT DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>2</sup>	Number <sup>4</sup>	Kind Code <sup>3</sup> (if known)			

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
HD		Tsutsui et al., High Quantum Efficiency in Organic Light-Emitting Devices with Iridium- Complex as a Triplet Emissive Center," Japanese Journal of Applied Physics, Vol. 38, Part 2, No. 12B, December 15, 1999, pp. L1502-L1504.	

Examiner Signature	Hester Doty	Date Considered	6/18/06
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449A/PTO				Complete If Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/659,585
				Filing Date	September 10, 2003
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2813
				Examiner Name	Heather Doty
Sheet	1	of	2	Attorney Docket Number	0756-7196

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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
[Handwritten initials]		6,475,912		Harada	11/05/2002	
		2001/0015618		Yamazaki et al.	08/23/2001	
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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>4</sup>
		Office <sup>2</sup>	Number <sup>2</sup>	Kind Code <sup>2</sup> (if known)				
[Handwritten Initials]		JP	2000-058650			02/25/2000		Abst.
		JP	2001-236025			08/31/2001		Abst.
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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s); publisher, city and/or country where published.		T <sup>4</sup>
LD		M.A. Baldo et al., "Very High-Efficiency Green Organic Light-Emitting Devices Based on Electrophoresence," Applied Physics Letters, Vol. 75, No. 1, July 5, 1999, pp. 4-6		
PD		Tsutsui et al., High Quantum Efficiency in Organic Light-Emitting Devices with Iridium-Complex as a Triplet Emissive Center," Japanese Journal of Applied Physics, Vol. 38, Part 12B, December 15, 1999, pp. L1502-L1504 abstract only		
LD		M.A. Baldo et al., "Highly Efficient Phosphorescent Emission from Organic Electroluminescent Devices," Nature, Vol. 395, September 10, 1998, pp. 151-154		

Examiner Signature	Heather Doty	Date Considered	10/3/06
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Substitute for form 1449A/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	10/659585
				Filing Date	September 10, 2003
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2813
				Examiner Name	Heather Doty
Sheets	2	of	2	Attorney Docket Number	0756-7196

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		Number	Kind Code <sup>2</sup> (if known)			
[Handwritten marks]		6,015,724		Yamazaki	01/18/2000	
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Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patente or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Office <sup>2</sup>	Number <sup>2</sup> Kind Code <sup>2</sup> (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					T <sup>3</sup>
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
HD		Stanley Wolf "4.5.6.3 Contact-Hole and Via Filling with Selective Electroless Metal Deposition," Silicon Processing for the VLSI Era, Vol. 2, Process Integration, pp. 256-257			
HD		M. Hatano et al., "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT with High Reliability and Performance," IEDM 97, pp. 523-526			
HD		U.S. Patent Application Serial No. 10/245,701 entitled Semiconductor Device and Method of Manufacturing the Same filed 09/18/2002			

Examiner Signature	Heather A. Doty	Date Considered	10/3/05
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